REMARKS

Applicants respectfully request reconsideration of the above-identified application in view of the foregoing amendments and following remarks.

I. Status of the Claims

Claims 1-5, 8 and 9 are pending. By this paper, claim 1 is amended to recite, *inter alia*, "closed loop forming circuits disposed at two opposite ends of the display panel and at another two opposite ends of the display panel." Support for the amendment may be found throughout applicants' specification as originally filed, including for example at page 14 line 5 to page 15, line 17; page 15, lines 17 to 27; and page 16, lines 1 to 19.

II. Rejections under 35 U.S.C. §103

Claim 1 was rejected under 35 U.S.C. §103(a) as unpatentable over Sudo et al. (U.S. Patent No. 5,693,913, herein "Sudo") in view of Yoshida et al. (U.S. Patent No. 5,798,756, herein "Yoshida"). Claims 2 and 3 were rejected under 35 U.S.C. §103(a) as unpatentable over Sudo in view of Yoshida in view of Oda (U.S. Patent No. 5,646,377). Claim 4 was rejected under 35 U.S.C. §103(a) as unpatentable over Sudo in view of Yoshida in view of Morita (U.S. Patent No. 5,128,499). Claim 5 was rejected under 35 U.S.C. §103(a) as unpatentable over Sudo in view of Yoshida in view of Watanabe et al. (U.S. Patent No. 5,567,920, herein "Watanabe"). Claims 8 and 9 were rejected under 35 U.S.C. §103(a) as unpatentable over Sudo in view of Yoshida, and further in view of Kawai (U.S. Patent Application Publication No. 2003/0086149).

The Applicants respectfully traverse the rejections of claims 1-5, 8 and 9. As explained more fully below, the requirements for such rejections are not met. In particular, the references

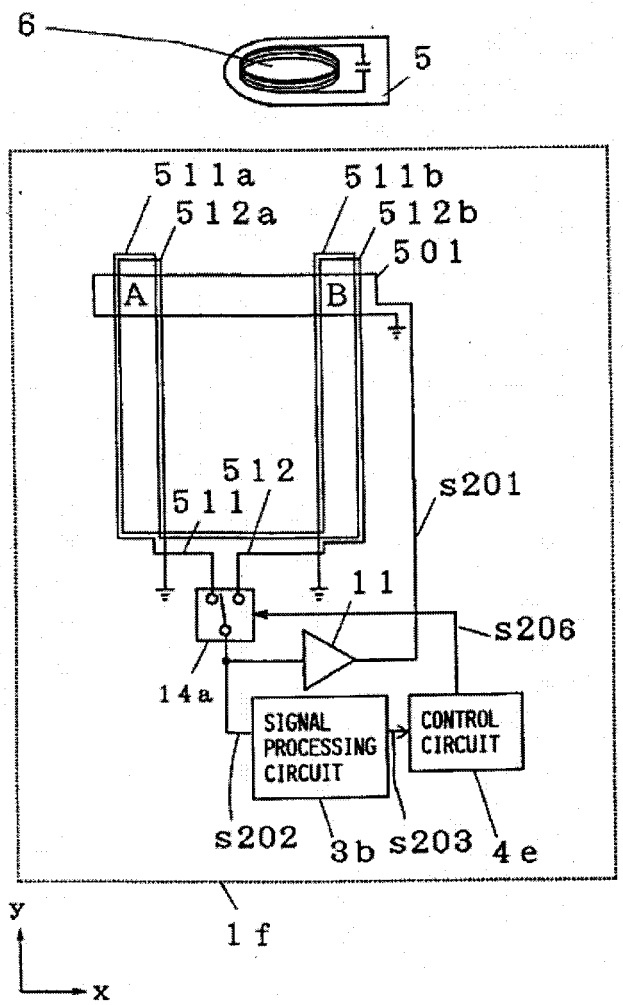
do not teach disclose or suggest "closed loop forming circuits disposed at two opposite ends of the display panel and at another two opposite ends of the display panel."

Applicants' claim 1 recites, *inter alia*:

> a display panel provided with a plurality of X interconnecting lines and a plurality of Y interconnecting lines disposed to intersect with each other in a matrix fashion;
>
> display drive circuits for supplying drive signals to the X and Y interconnecting lines in a display drive mode;
>
> closed loop forming circuits disposed at two opposite ends of the display panel and at another two opposite ends of the display panel;
>
> switching circuits connected to a terminal of each of the X and Y interconnecting lines, said switching circuits connecting the X or Y interconnecting lines to the display drive circuits in the display drive mode and to the closed loop forming circuits in a coordinate detection drive mode; and
>
> a detection circuit for detecting signals outputted from the closed loop forming circuits in the coordinate detection drive mode in response to a position indicator indicating a position in a coordinate input area of the display panel where the X interconnecting lines and the Y interconnecting lines are disposed in the matrix fashion;
>
> wherein, in the coordinate detection drive mode,
>
> the closed loop forming circuits disposed at two ends of the display panel connect at least a pair of terminals of the X interconnecting lines in each end to form a multiple closed loop of the X interconnecting lines, and
>
> the closed loop forming circuits disposed at another two ends of the display panel connect at least a pair of terminals of the Y interconnecting lines at each end to form a multiple closed loop of the Y interconnecting lines.

Sudo describes a coordinate detection apparatus that is capable of accurately detecting a position even in the peripheral portion of the coordinate detector. [Sudo at col.2 lns.52-60]. Amended independent claim 1 is directed to a coordinate input apparatus. The Office Action indicated that elements 511 and 512 within Figure 7 of Sudo constitute a multiple closed loop. Figure 7 is reproduced on the following page:

F I G. 7



Although the Figures of Sudo disclose circuitry for a coordinate detection apparatus, Applicant's review finds no disclosure of the arrangement of the circuitry in relation to the display panel itself. Thus, as Sudo is silent on the issue, even assuming *arguendo* that Figure 7 discloses a multiple closed loop, it is believed that Sudo does not teach disclose or suggest "closed loop forming circuits disposed at two opposite ends of the display panel and at another two opposite ends of the display panel."

As to the other references, Oda describes a point detecting device transmitting signals from both ends of the loop coil arrangement towards the pointing device, and/or receives at both ends the signals generated by the electromagnetic function between the loop coil arrangement and the pointing device.[1] Morita describes loops that are embedded in each other as shown in FIG. 1. Watanabe describes a position detection apparatus in which a multiple loop is formed as shown in FIG. 18. Yoshida describes that X and Y electrodes of a liquid crystal display are utilized as electrodes of a position detection apparatus by a selector switch. Kawai is merely cited as teaching at page 1, paragraph [0003], an electrophoretic display having a memory characteristic.[2] Applicants review of the remaining references find no disclosure of "closed loop forming circuits disposed at two opposite ends of the display panel and at another two opposite ends of the display panel."

Therefore, amended claim 1 and claims dependent therefrom are patentably distinguishable over the cited references, either taken individually or in combination. Accordingly, the Applicants respectfully request the withdrawal of the rejections of claims 1-5, 8 and 9.

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[1] See, for example, the Abstract of Oda.

[2] See the outstanding Office Action at page 9, lines 1 and 2.

CONCLUSION

In view of the above amendments, the Applicants believe the pending application is in condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

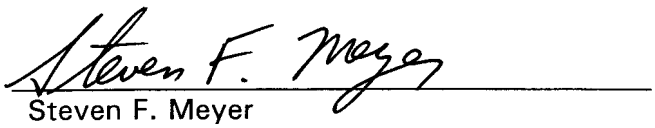
The Examiner is respectfully requested to contact the undersigned at the telephone number indicated below if the Examiner believes any issue can be resolved through either a Supplemental Response or an Examiner's Amendment.

AUTHORIZATION

Based on the foregoing amendments and remarks, Applicant respectfully requests reconsideration and withdrawal of the rejection of claims and allowance of this Application. In the event that any issues remain that could potentially be resolved by telephone, the Examiner is urged to contact the undersigned at the number indicated below. Should any additional fee(s) be required for the entry of this Amendment, the Commissioner is hereby authorized to charge Deposit Account No. ***50-4827***, Order No. 1232-5791.

Respectfully submitsted,
Locke Lord Bissell & Liddell LLP

Dated: February 17, 2009

By: 
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